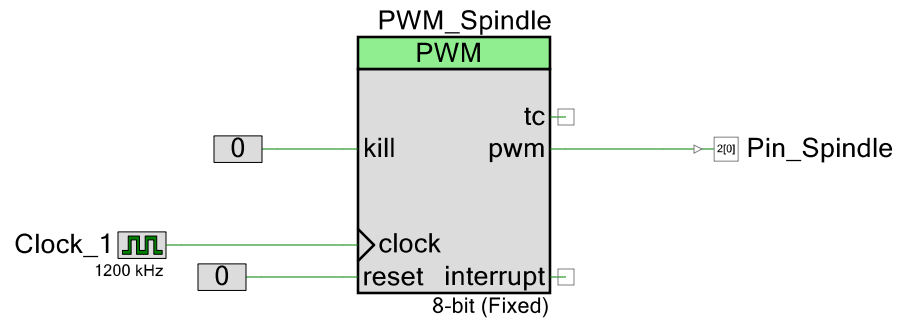


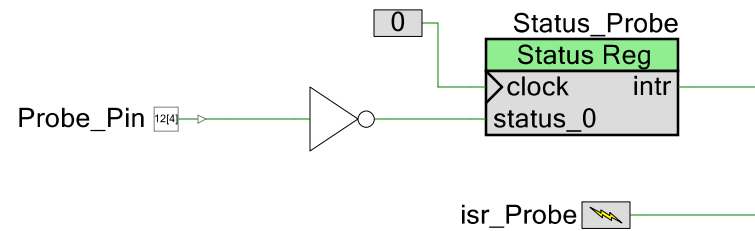
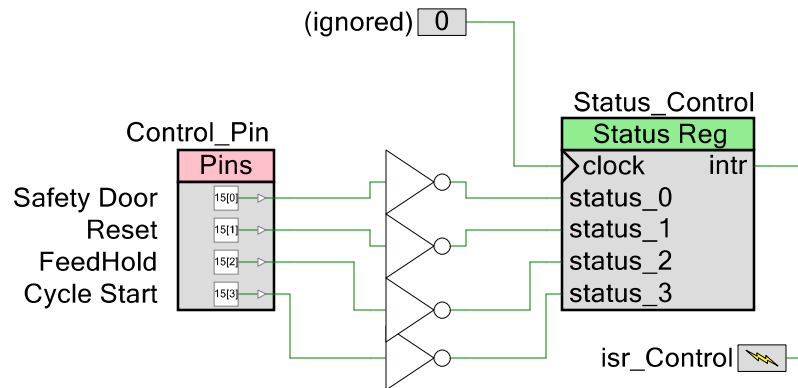
Logical Port Pin	LCD Module Pin
LCDPort_0	DB4
LCDPort_1	DB5
LCDPort_2	DB6
LCDPort_3	DB7
LCDPort_4	E
LCDPort_5	RS
LCDPort_6	R/W



- 2[11] Pin_Spindle_Direction
- 2[2] Pin_Spindle_Enable
- 2[7] Flood_Coolant_Pin
- 2[6] Mist_Coolant_Pin

Inputs

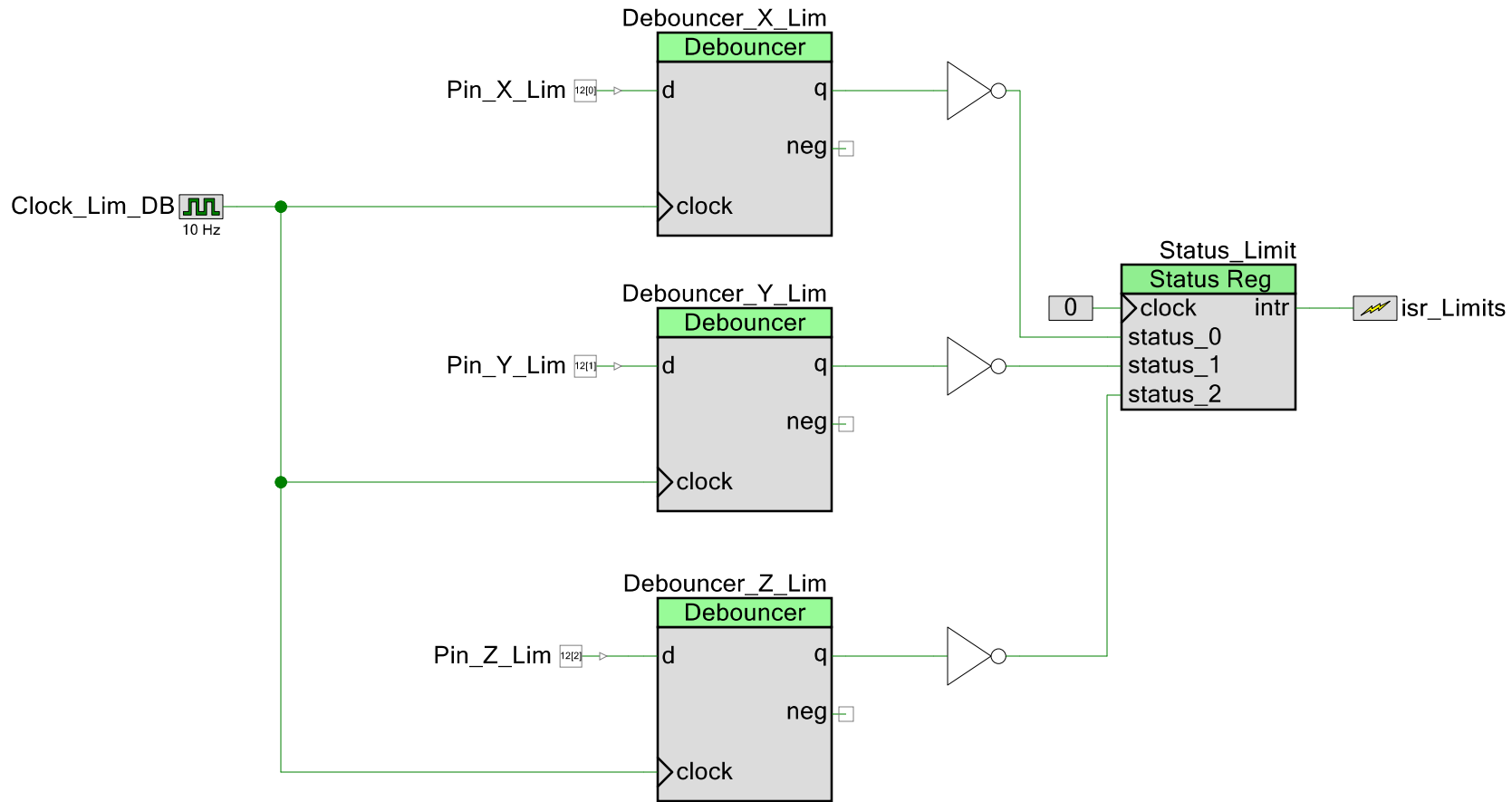
Use Control register so "nots" can be added when logic is inverted



Limit Switches:

Set the debounce time with the the cloclck period.

Add a "Not" before the status register if the switches close to ground



Outputs

